

Figure 1

AUS920000742US1

Page 1 of 4

The diagram illustrates a computer system architecture. At the top, two processors, Processor 202 and Processor 204, are connected to a horizontal System Bus 206. Below the system bus is a block containing a Memory Controller/Cache 208 and an I/O Bridge 210. Local Memory 209 is connected to the memory controller/cache. A vertical I/O Bus 212 extends from the I/O bridge. On the left side of the I/O bus, a Graphics Adapter 230 and a Hard Disk 232 are connected. On the right side, three PCI Bus Bridges are connected: 214, 222, and 224. Each PCI bus bridge is connected to a horizontal PCI Bus. Bridge 214 is connected to a Modem 218 and a Network Adapter 220. Bridge 222 and Bridge 224 are connected to their respective PCI buses without additional components shown. Reference numerals 200, 216, 226, and 228 point to the system bus, the first PCI bus, the second PCI bus, and the third PCI bus, respectively.

AUS920000742US1
Page 2 of 4

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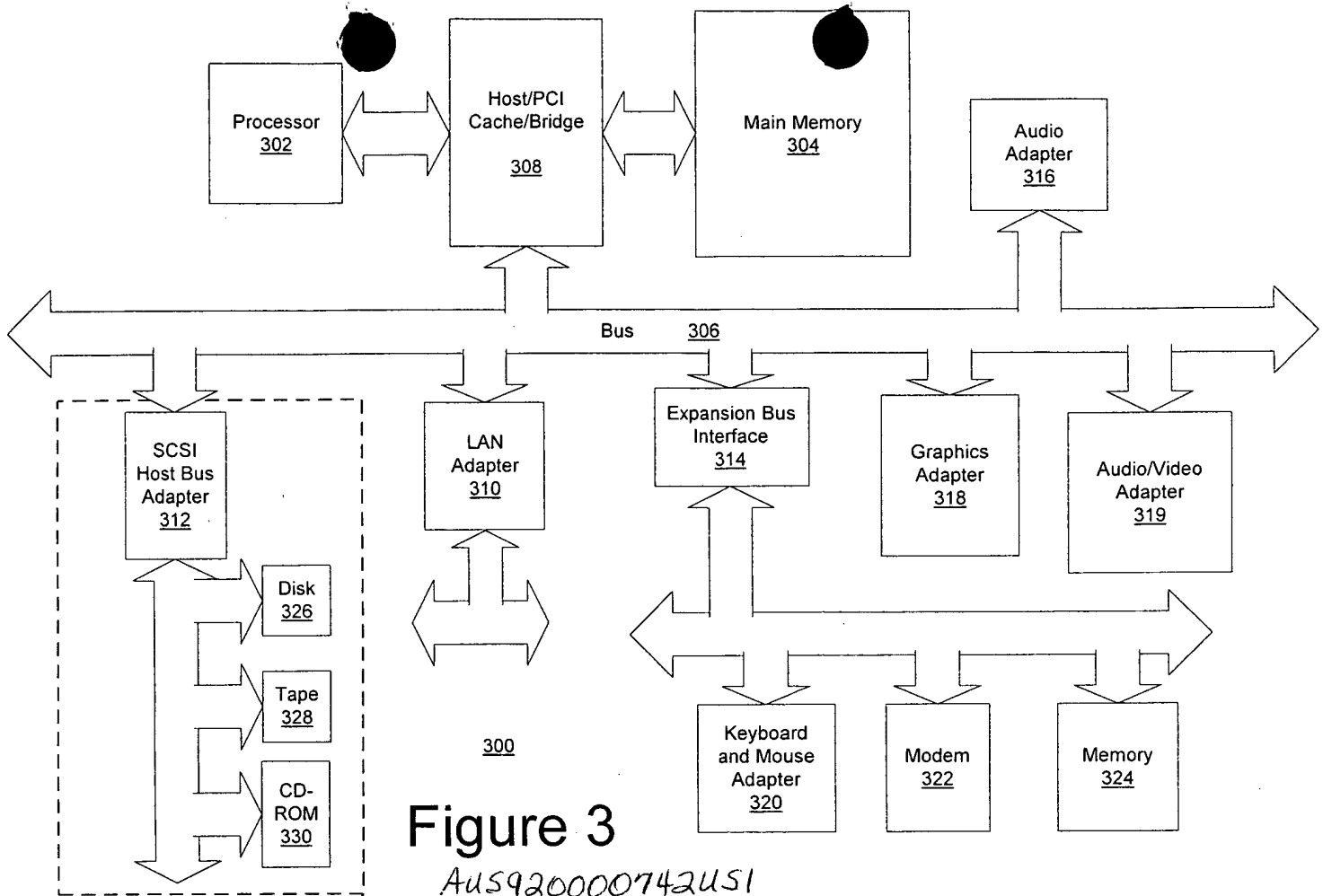


Figure 3

AUS920000742US1

Page 3 of 4

332

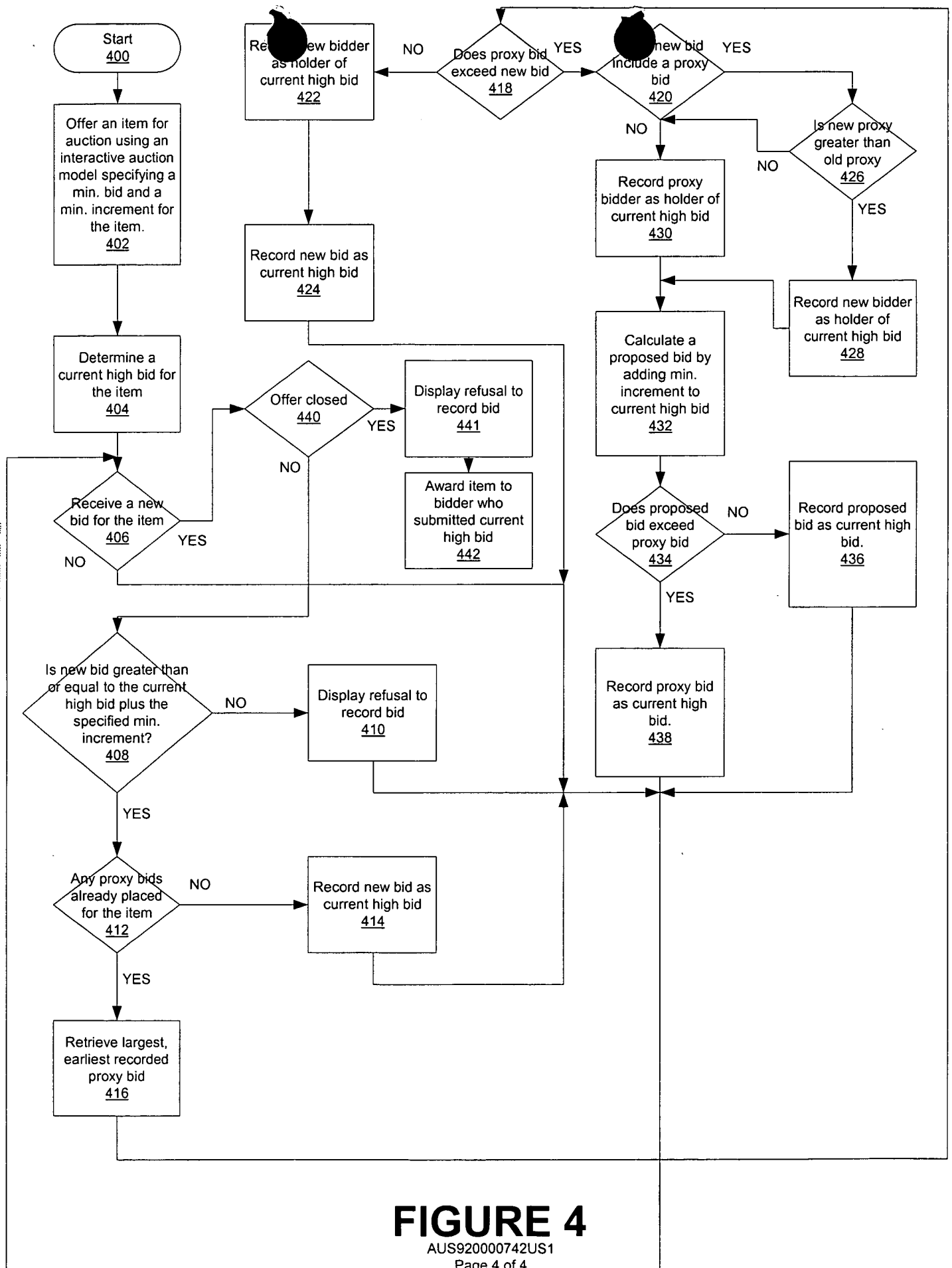


FIGURE 4

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